



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Ray M. Richardson	§ Group Art Unit:	2182
Serial No.:	09/964,681	§	
Filed:	September 27, 2001	§ Examiner:	Eron J. Sorrell
For:	Controlling Data Flow Between Processor Systems	§ Atty. Dkt. No.:	ITL.0607US (P11748)
		§ Assignee:	Intel Corporation

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT SUBMITTED WITH RCE**

Sir:

This paper is submitted in conjunction with the enclosed Request for Continuing Examination (RCE) in the above-identified Patent Application. Further examination and reconsideration are respectfully requested in view of the Amendments and Remarks set forth below.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.

Date of Deposit: December 9, 2005
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as <b>first class mail</b> with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Jennifer Juarez